

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	(processor adj suppl\$3) near5 clock adj (rate speed)	USPAT	OR	OFF	2006/08/08 18:15
S2	0	(processor adj suppl\$3) same clock adj (rate speed)	USPAT	OR	OFF	2006/08/08 18:15
S3	2	(cpu adj suppl\$3) same clock adj (rate speed)	USPAT	OR	OFF	2006/08/08 18:15
S4	2	(cpu adj suppl\$3) same clock adj (rate speed)	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2006/08/08 18:16
S5	10	over\$1clock\$3 near2 (prevent\$3 detect\$3)	USPAT	OR	OFF	2006/08/08 18:18
S6	53	determin\$3 near3 (clock adj (speed rate)) near3 (cpu processor)	USPAT	OR	OFF	2006/08/08 18:40
S7	4	determin\$3 near3 (clock adj (speed rate)) near3 (cpu processor) near7 (register memory)	USPAT	OR	OFF	2006/08/08 18:39
S8	5	provid\$3 near3 (clock adj (speed rate)) near3 (cpu processor)	USPAT	OR	OFF	2006/08/08 18:40
S9	5	motherboard with (processor cpu) with (register memory) with (clock adj (rate speed frequency))	USPAT	OR	OFF	2006/08/08 18:43
S10	92	clock adj (rate speed frequency) near3 suppl\$3 near3 (processor cpu)	USPAT	OR	OFF	2006/08/08 18:49
S11	9	clock adj (rate speed frequency) near3 suppl\$3 near3 (processor cpu) near5 (motherboard component\$1 circuit\$2)	USPAT	OR	OFF	2006/08/08 18:50
S12	4	("5841287").URPN.	USPAT	OR	OFF	2006/08/08 19:00
S13	0	(motherboard board chipset) near3 (read\$3 determin\$3) near3 (clock adj (speed rate frequency)) near3 (cpu processor)	USPAT	OR	OFF	2006/08/08 19:09
S14	0	(motherboard board chipset) near3 (access\$3 provid\$3) near3 (clock adj (speed rate frequency)) near3 (cpu processor)	USPAT	OR	OFF	2006/08/08 19:10
S15	30	(motherboard board chipset) near3 (clock adj (speed rate frequency)) near3 (cpu processor)	USPAT	OR	OFF	2006/08/08 19:10